


120-09356-0 PRIMARY SIDE ASSEMBLY

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1	INITIAL RELEASE	10/13/10	RKAD
2	MODIFIED CTS, RTS	01/28/11	RKAD
*A	CORRECTED RTS, CTS	01/28/11	RKAD
*B	ADDED EOS PROTECTION CIRCUITS ON EBK CONNECTORS. ADDED C54 & C55. CHANGED FOOTPRINT POT R56. ADDED J43 & J44.	02/13/12	RKAD

UNLESS OTHERWISE SPECIFIED		SIGNATURES		DATE	COMPANY:		198 CHAMPION COURT SAN JOSE, CA 95134 (408) 943-2600		
DIMENSIONS ARE IN INCHES TOLERANCES ON: ANGLES +/- .2 2 PL DECIMALS +/- .010 3 PL DECIMALS +/- .005 THIRD ANGLE PROJECTIONS 		DRAWN : ZMK		02/13/12	CYPRESS SEMICONDUCTOR		ASSEMBLY DRAWING CY8CKIT-050 PSoC 5 Development Kit		
		CHECKED : RKAD		02/13/11					
		ENGRG : PAVA		02/13/11					
		ISSUED							
CYPRESS PROPRIETARY					SIZE		FSCM NO	DWG NO	REV
THIS DOCUMENT CONTAINS CONFIDENTIAL, PROPRIETARY INFORMATION THAT IS CYPRESS SEMICONDUCTOR PROPERTY. DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS EXCEPT AS AUTHORIZED BY CYPRESS.					C			120-09356-0	*B
					SCALE: 1/1				SHEET:

4

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D

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C

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B

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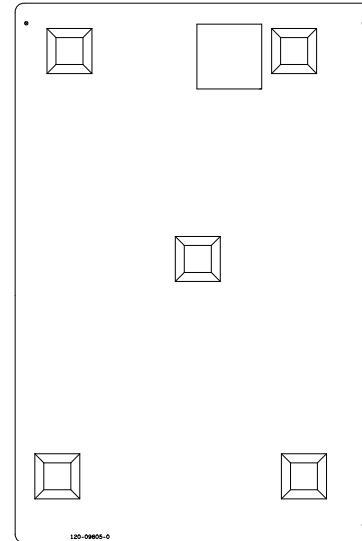
A

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3

2

1



120-09356-0 SECONDRY SIDE ASSEMBLY

		198 CHAMPION COURT SAN JOSE, CA 95134 (408) 943-2600	
ASSEMBLY DRAWING CY8CKIT-030 PSoC 3 Development Kit			
SIZE C	FSCM NO	DWG NO 120-09356-0	REV *B
SCALE: 1/1		SHEET: 2 OF 2	